

Yeshwantrao Chavan College of Engineering

(An Autonomous Institution affiliated to R.T.M. Nagpur University)

ESE Odd Term 2026-27

End Semester Theory Examination :

V1.0
01.10.2026
VLSI

Programme /Date	13/11/2026	14/11/2026	16/11/2026	17/11/2026	18/11/2026	19/11/2026	20/11/2026	21/11/2026
Shift Time	Shift 1	Shift 1	Shift 1	Shift 1	Shift 1	Shift 1	Shift 1	Shift 1
B Tech Sem-III VLSI SoE-23VLSI-101		23VLS1306/ 23EE1306 : Basics of Python Programming		23GE1301: Fundamentals of Management and Economics		23VLS1301: Analog Circuits		
B Tech Sem-V VLSI SoE-23VLSI-101	23VLS1501: Embedded System		23VLS1505 /23EE1503: Digital System Modelling		23VLS1503: CMOS VLSI Design		: Open Elective-III	
B Tech Sem-VII VLSI SoE-23VLSI-101		23VLS1701-Analog and Digital Communication		23VLS1703-System Verilog for Design & Verification		23VLS1705- Introduction to VLSI lifecycle		23VLS1721-PE-IV : Machine Learning for VLSI; 23VLS1723-PE- IV : Internet of Things ; 23VLS1725-PE-IV : Hardware Security;

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Programme /Date	23/11/2026	25/11/2026	26/11/2026	27/11/2026	28/11/2026	30/11/2026	02/12/2026
Shift Time	Shift 1	Shift 1	Shift 1	Shift 1	Shift 1	Shift 1	Shift 1
B Tech Sem-III VLSI SoE-23VLSI-101		MDM-1		OE-I		23VLS1303 /23EE1303 : Network Analysis	23VLS1305 / 23EE1305 : Signal and Systems
B Tech Sem-V VLSI SoE-23VLSI-101	MDM-3		23VLS1521: PE-I : CAD for VLSI; 23VLS1522: PE-I : Algorithm and Data Structure; 23VLS1523: PE-I : Semiconductor Device Modeling;		23VLS1507: Object Oriented Programming		
B Tech Sem-VII VLSI SoE-23VLSI-101		MDM-5		23VLS1741-PE-V : Low Power VLSI; 23VLS1742-PE-V : VLSI Signal Processing; 23VLS1743- PE-V : Mixed Signal IC Design			