

V 1.00

Yeshwantrao Chavan College of Engineering

19.05.2025

(An Autonomous Institution affiliated to R.T.M. Nagpur University)

B Tech FY

ESE Even Term 2024-25

Sem 1B Phy Group SOE 23-101

3

End Semester Theory Examination :

23-05-2025 V1.1

Programme /Date	09/06/2025	11/06/2025	14/06/2025	17/06/2025	19/06/2025
Shift Time	Shift 2	Shift 2	Shift 1	Shift 1	Shift 1
B Tech-Sem-I (B) _SOE_23 ME-101	23GE1102: Differential Equations, Matrices and Statistics	23GE1108: Engineering Physics	23ME1105: Material Science & Metallurgy	23EL1101 : Basic Electrical and Electronics Engineering	23ME1101: Engineering Graphics
B Tech-Sem-I (B) _SOE_23 EL-101	23GE1102: Differential Equations, Matrices and Statistics	23GE1108: Engineering Physics	23EL1103: Fundamentals of Electrical Engineering	23EL1101 : Basic Electrical and Electronics Engineering	23ME1101: Engineering Graphics
B Tech-Sem-I (B) _SOE_23 CT-101	23GE1103: Differential Equations and Complex Analysis	23GE1110: Applied Physics	23CT1101: Introduction to Logic Building and Programming	23EL1101 : Basic Electrical and Electronics Engineering	23ME1101: Engineering Graphics
B Tech-Sem-I (B) _SOE_23 CSE-101	23GE1103: Differential Equations and Complex Analysis	23GE1110: Applied Physics	23CSE1101: Object Oriented Programming using Python	23EL1101 : Basic Electrical and Electronics Engineering	23ME1101: Engineering Graphics
B Tech-Sem-I (B) _SOE_23 CSD-101	23GE1103: Differential Equations and Complex Analysis	23GE1110: Applied Physics	23IT1101: Basics of Python Programming	23EL1101 : Basic Electrical and Electronics Engineering	
B Tech-Sem-I (B) _SOE_23 CSE(IOT)-101	23GE1102: Differential Equations, Matrices and Statistics	23GE1110: Applied Physics	23EE1103: Digital Logic Design	23EL1102: Basic Electrical Engineering	23EE1101: Basic Electronics
B Tech-Sem-I (B) _SOE_23 VLSI-101	23GE1102: Differential Equations, Matrices and Statistics	23GE1108: Engineering Physics	23EE1103: Digital Logic Design	23EL1102: Basic Electrical Engineering	23EE1101: Basic Electronics

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Check Dates & Shift Timings

Programme /Date	19/06/2025	20/06/2025	20/06/2025	21/06/2025	21/06/2025	23/06/2025	23/06/2025	24/06/2025	24/06/2025
Shift Time	Shift 2	Shift 1	Shift 2	Shift 1	Shift 2	Shift 1	Shift 2	Shift 1	Shift 2
B Tech-Sem-I (B) _SOE_23 ME-101	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics
B Tech-Sem-I (B) _SOE_23 EL-101	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics
B Tech-Sem-I (B) _SOE_23 CT-101	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics
B Tech-Sem-I (B) _SOE_23 CSE-101	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics	23ME1101: Engineering Graphics
B Tech-Sem-I (B) _SOE_23 CSD-101				23ME1103: Engineering Design	23ME1103: Engineering Design	23ME1103: Engineering Design			
B Tech-Sem-I (B) _SOE_23 CSE(IOT)-101									
B Tech-Sem-I (B) _SOE_23 VLSI-101									

Sd/- (Prof D. R. Raut)

NOTE: Regularly Visit <https://www.ycce.edu/ExaminationTimetable.php>  
Keep admit card, Identity card on desk during exam

Shift I : 10 am to 1 pm  
Shift II: 2 to 5 pm

Controller of Examinations