



Sr No.	Seat No.	Reg No.	Student Name	Course wise Grade obtained in this Examination	Result	SEMESTER					CUMULATIVE			
						CR	ECR	EGRP	SGPA	EQUI. %	CECR	CEGRP	CGPA	Equi. % for complete program
1	381351	23330001	ASHWINI KAMESHWAR GAJBHIYE	23VD201(A), 23VD202(A), 23VD203(A), 23VD204(A), 23VD205(A+), 23VD212(B+), 23VD221(B+), 23VD232(A+)	PASS	20	20	179.50	8.98	82.25				
2	381352	23330002	AYUSH RAJHANS CHAKOLE	23VD201(B+), 23VD202(A), 23VD203(B), 23VD204(A), 23VD205(A+), 23VD212(B+), 23VD221(B+), 23VD232(B)	PASS	20	20	165.25	8.26	75.13				
3	381353	23330003	DEVIKA SAHU	23VD201(C), 23VD202(B), 23VD203(B), 23VD204(A), 23VD205(A), 23VD212(B), 23VD221(B+), 23VD232(B)	PASS	20	20	150.75	7.54	67.88				
Vertical Sum						60	60	495.50	24.78	225.30				
Grand Sum						60	60	495.50	24.78	225.30				

Prepared By	Name	Signature	Approved By	Name	Signature	Scrutinized By	Name	Signature
TC Chairman			RRMC Chairman			Scrutinizer 1		
TC Member 1			Member 1			Scrutinizer 2		
TC Member 2			Member 2					

By order of EC

Controller of Examinations

Grades:--> =-1, A+=10, A=9, B+=8.25, B=7.50, C+=6.75, C=6, D=5, F=0, F=0, CC=0, I=0, W=WithHeld, Z=Non Completion in course requirement, X=Extension in dissertation, G=Successful Completion of Audit Course, H=Non Completion of Audit

Course Code Course Name(Credits)

23VD205 - Analog IC Design Laboratory (1.0)	23VD202 - Analog IC Design (3.0)	23VD212 - ASIC Design (3.0)	23VD204 - RF Circuit design Laboratory (1.0)
23VD201 - RF Circuit design (3.0)	23VD203 - System Verilog for Verification (3.0)	23VD221 - Verification & Testing of VLSI Circuit (3.0)	23VD232 - Nano Scale MOS Transistors (3.0)

Abbreviation : ECR=Earned Credits, EGRP=Earned Grade Point, SGPA=Semester Grade Point Average, CGPA=Cumulative Grade Point Average