RESULT GAZETTE WITHOUT CGPA (Ex-Students)

NAGAR YUWAK SHIKSHAN SANSTHA

Yeshwantrao Chavan College of Engineering (An Empowered Autonomous Institute Affiliated to RTM Nagpur University, Nagpur) NAAC Accredited A++ Hingna Road, Wanadongri, Nagpur- 441110

Name of ESE Programme

Semester

Scheme

| | | Seat No. | Reg No. | | | | | | SEMESTER | | | | | CUMULATIVE | | | |
|-------------|-----------|----------|----------|--------------|-----------|--|------|--------------|----------|-------------|--------|------|---------|------------|-------|----------|---------------------------------------|
| S N | Sr No. | | | Student Name | | Course wise Grade obtained in this Examination | | Result | CR | ECR | EGRP | SGPA | EQUI. % | CECR | CEGRP | CGPA | Equi. % for complete program |
| | 1 | 368724 | 23330003 | DEVIKA SAHU | | 23VD103(D), 23VD104(B+), 23VD105(B+), 23VD106(C), 23VD107(A), 23VD108(A), 23VD109(A) | | PASS | 21 | 21 | 161.25 | 7.68 | 69.29 | | | | |
| | | | | | | | | Vertical Sum | 21 | 21 | 161.25 | 7.68 | 69.30 | | | | |
| | | | | | | | | Grand Sum | 21 | 21 | 161.25 | 7.68 | 69.30 | | | | |
| Prepared By | | | | Name | Signature | Approved By | Name | Signature | \$ | Scrutinized | Ву | | Name | | | Signatur | e |
| - | C Ch | airman | | | | RRMC Chairman | | | Sci | utinizer 1 | | | | | | | |
| - | C Me | mber 1 | | | | Member 1 | | | Sci | utinizer 2 | | | | | | | |
| - | ГС Ме | mber 2 | | | | Member 2 | | | | | | | | | | | |

Grades:--> =-1, A+=10, A=9, B+=8.25, B=7.50, C+=6, D=5, F=0, F=0, C=0, I=0, W=WithHeld, Z=Non Completion in course requirement, X=Extension in dissertation, G=Successful Completion of Audit Course, H=Non Completion of Audit

Course Code Course Name(Credits)

23VD108 - Digital IC Design Laboratory (1.0) 23VD105 - Advanced Digital System Design (3.0) 23VD102 - Digital IC Design (3.0) 23VD107 - Embedded System & RTOS Laboratory (1.0) 23VD109 - Advanced Digital System Design Laboratory (1.0) 23VD106 - Sythesis & Optimisation of VLSI Circuits (3.0) 23VD104 - Embedded System & RTOS (3.0) 23VD101 - VLSI Signal Processing (3.0) 23VD103 - VLSI Technology (3.0)

Abbreviation : ECR=Earned Credits, EGRP=Earned Grade Point, SGPA=Semester Grade Point Average, CGPA=Cumulative Grade Point Average

- : ESE Odd Resit 2023-24
- : M. Tech. Electronics and Communication (VLSI Design)
- : 1
- : 2023

By order of EC

Controller of Examinations